



Atty. Docket No. RMBS.P101

PATENT

2634

IN THE UNITED STATES PATENT OFFICE

In Re Patent Application of:) Examiner: Not Yet Assigned

Chang, et al.) Art Unit: Not Yet Assigned

Application No. 09/941,079)

Filed: August 28, 2001)

For: CLOCK DATA RECOVERY WITH)
SELECTABLE PHASE CONTROL INPUT)

Assistant Commissioner for Patents
Washington, D.C. 20231

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Enclosed herewith for filing in the above-identified patent application are eleven
(11) sheets of formal drawings.

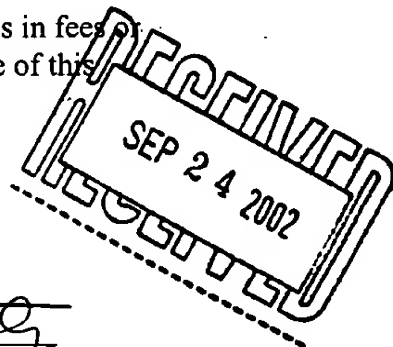
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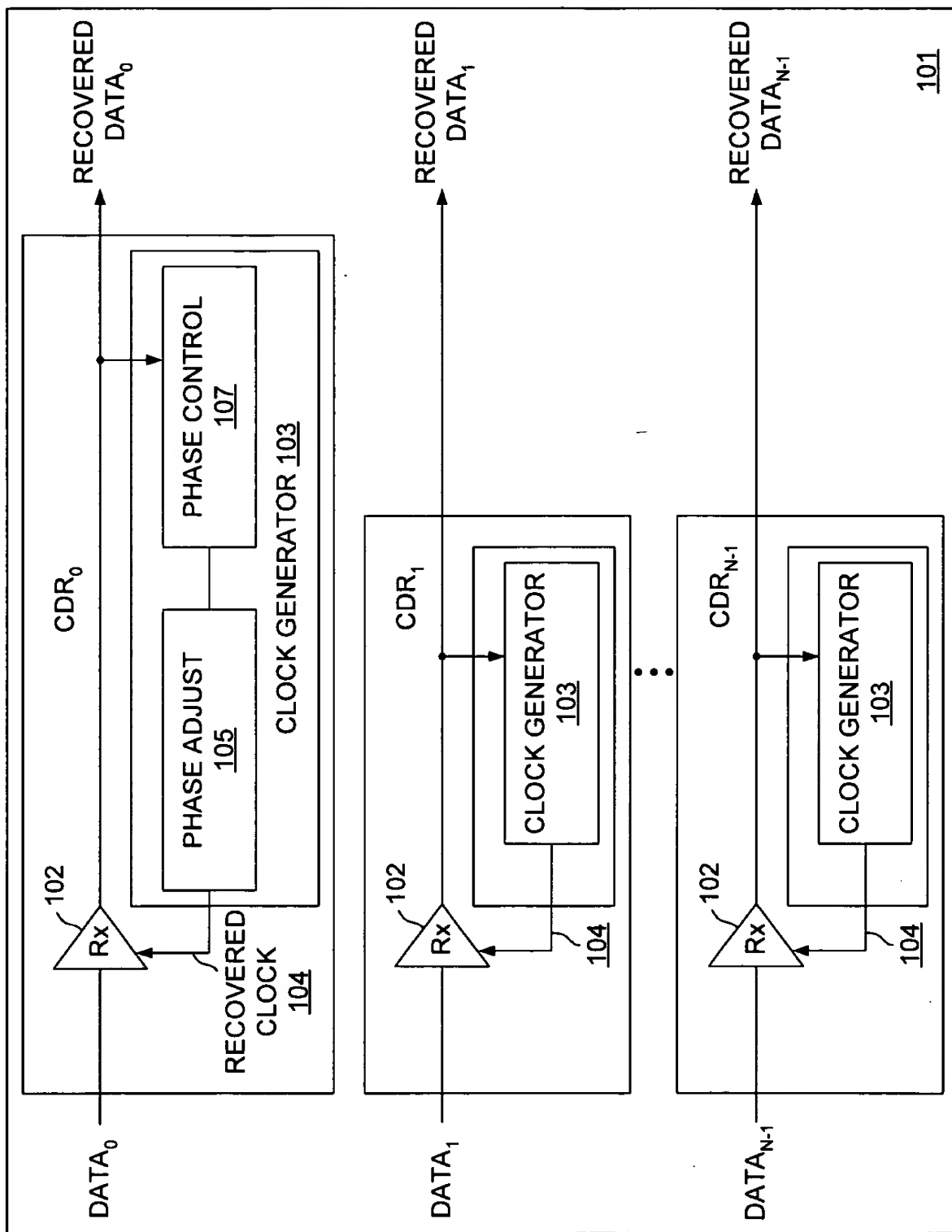


FIG. 1
 (Prior Art)

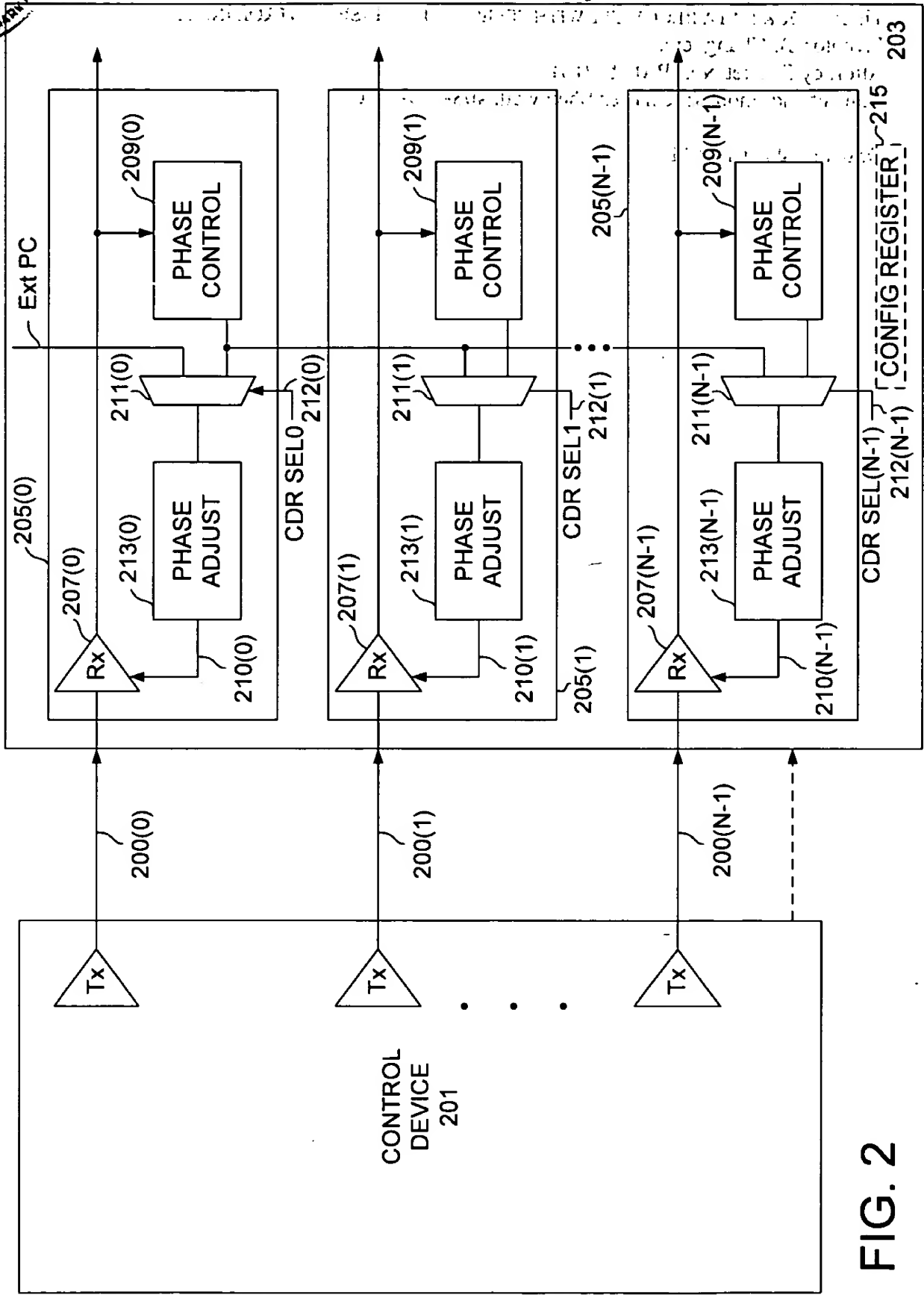


FIG. 2

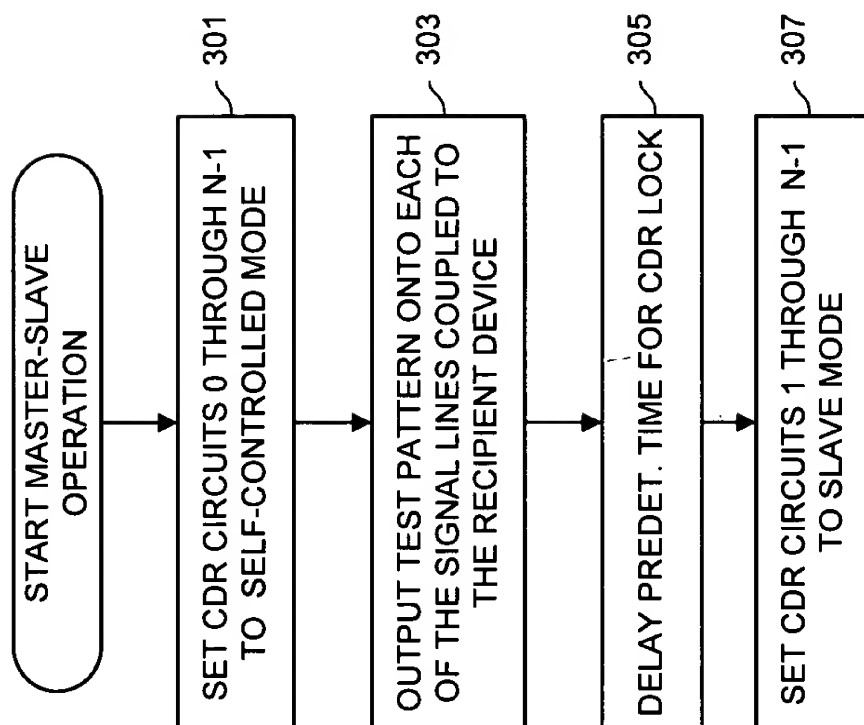


FIG. 3

1. A method for controlling a phase-locked loop (PLL) in a multi-channel system, comprising:
 receiving a reference signal;
 generating a feedback signal from the reference signal;
 comparing the reference signal and the feedback signal to generate an error signal;
 adjusting the phase of the reference signal based on the error signal; and
 outputting the adjusted reference signal.

FIG. 4 is a block diagram of a multi-channel PLL system.

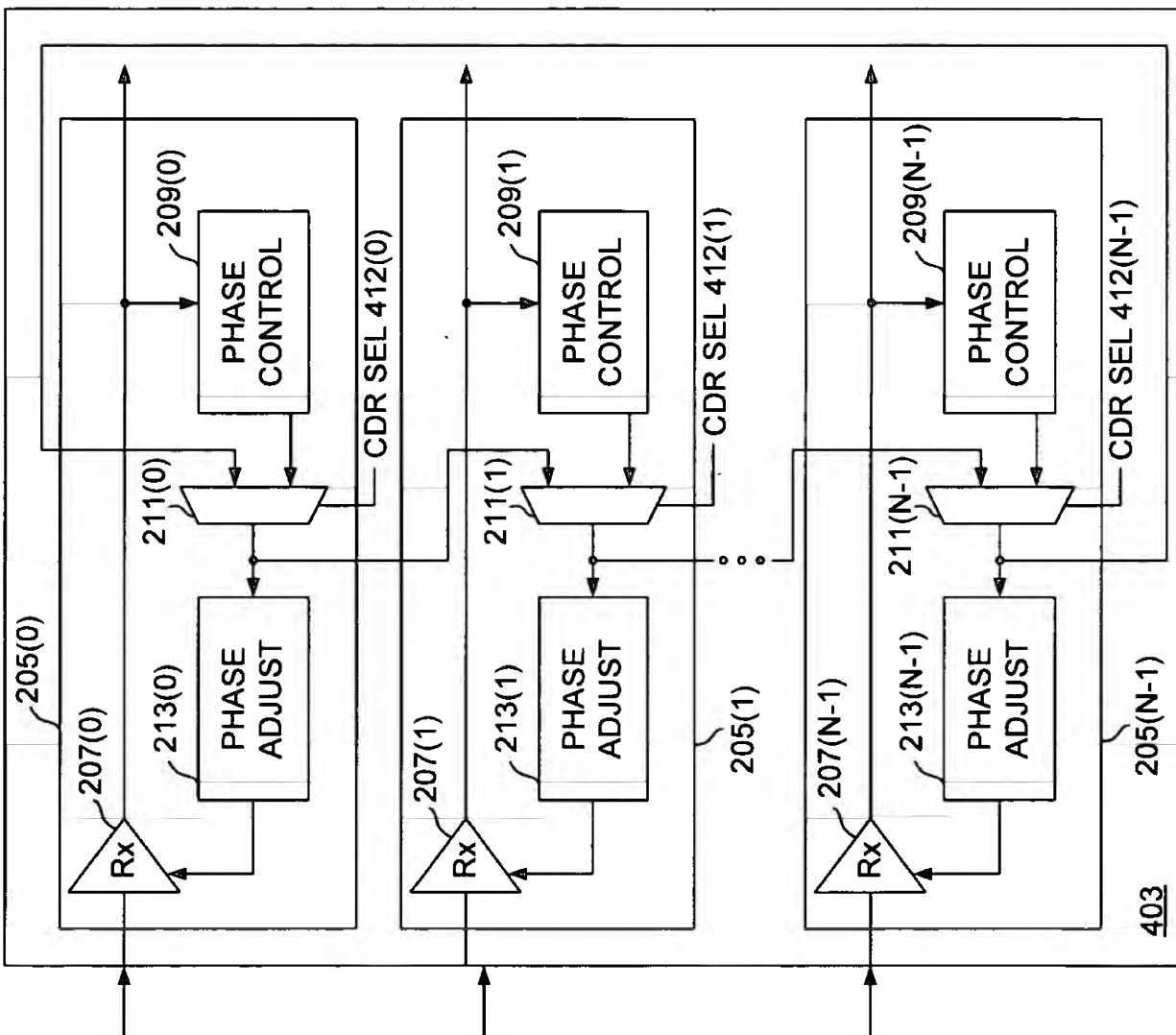


FIG. 4

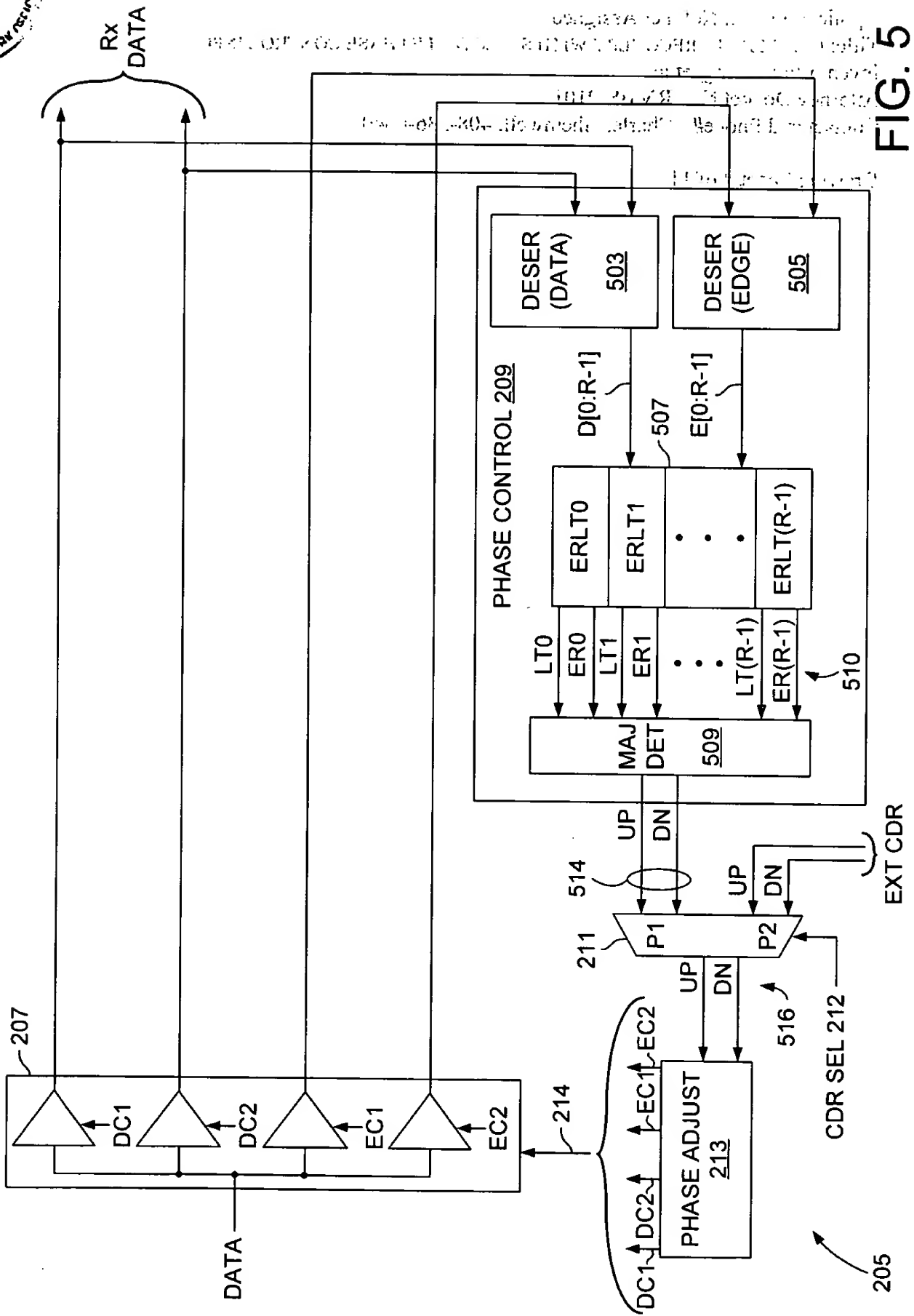
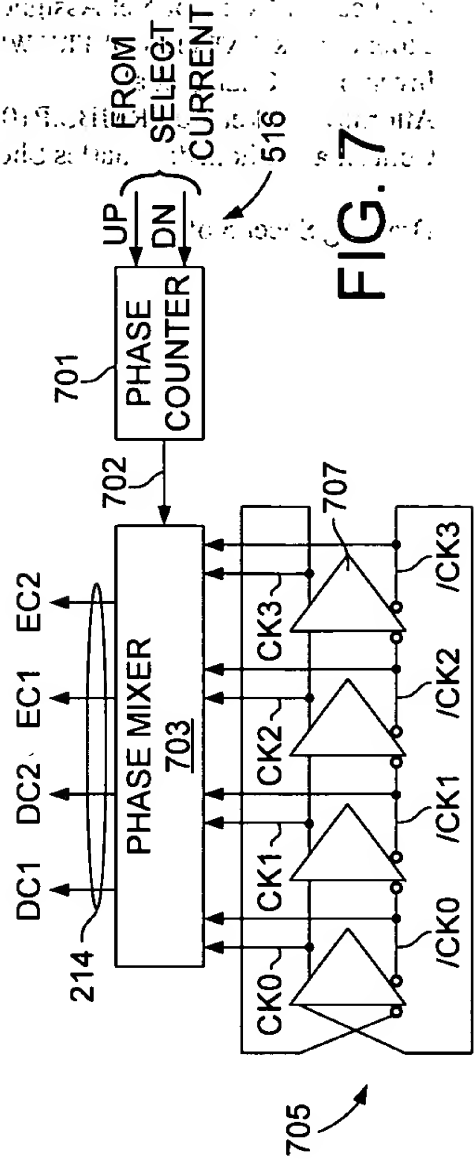
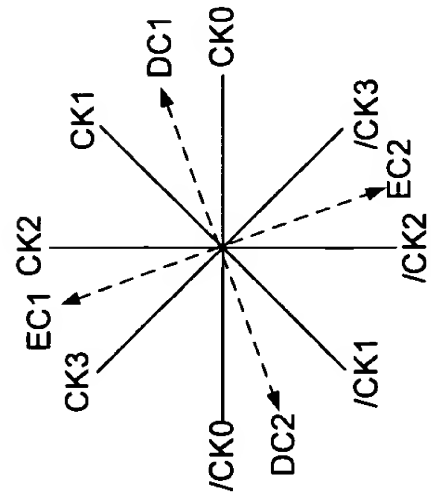
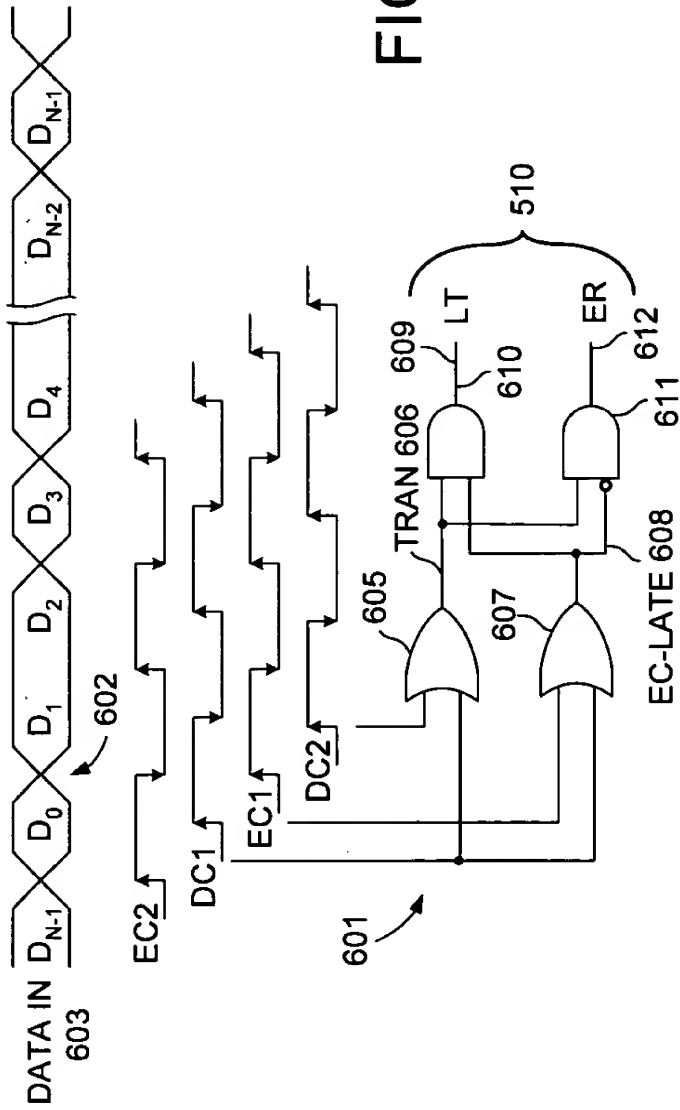
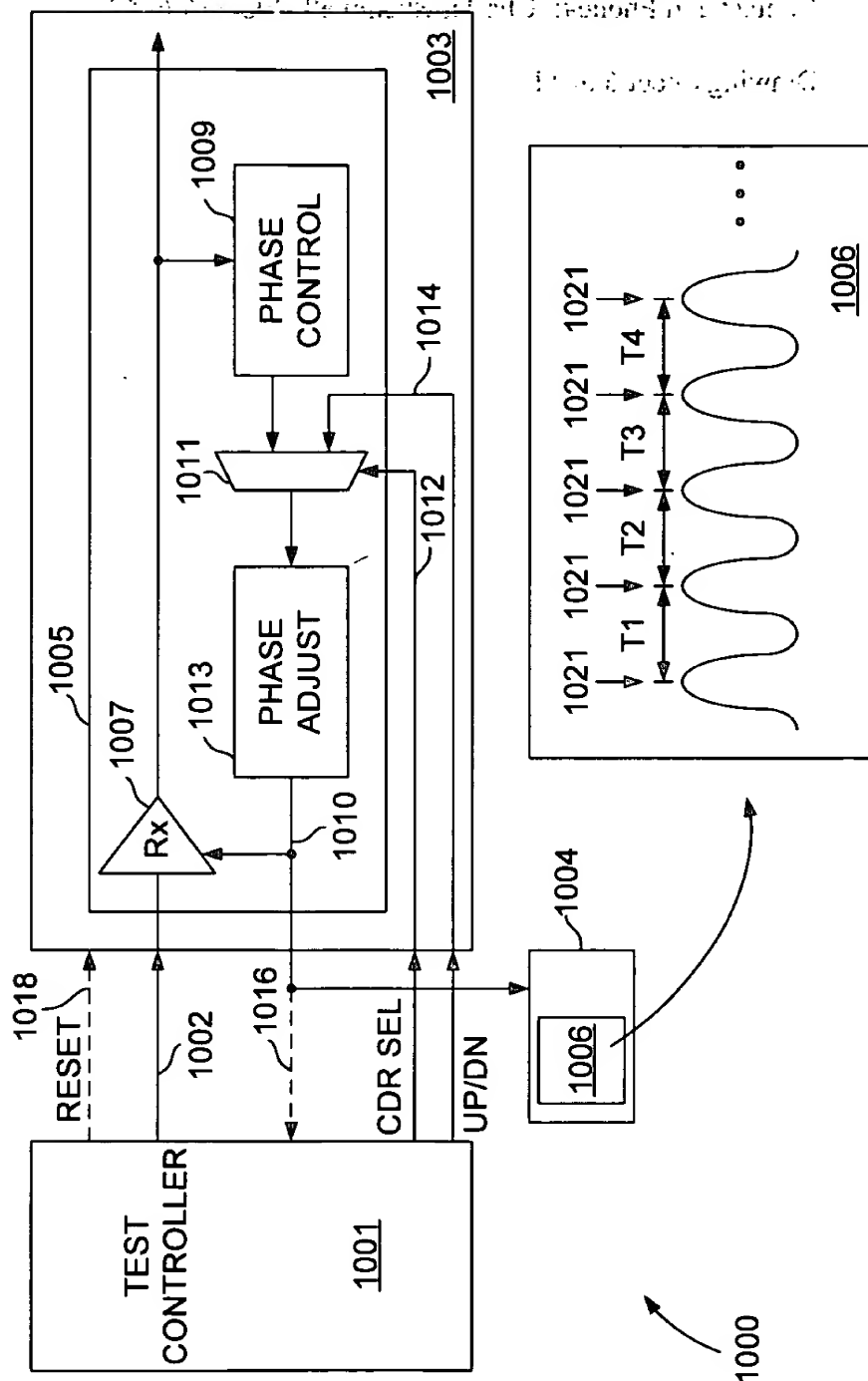
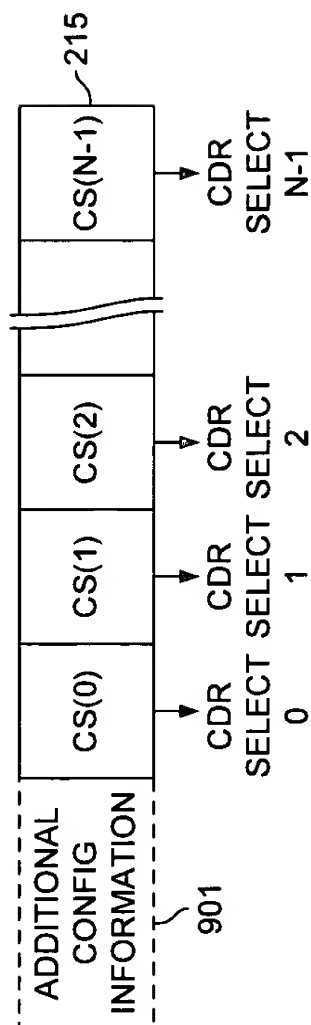


FIG. 5



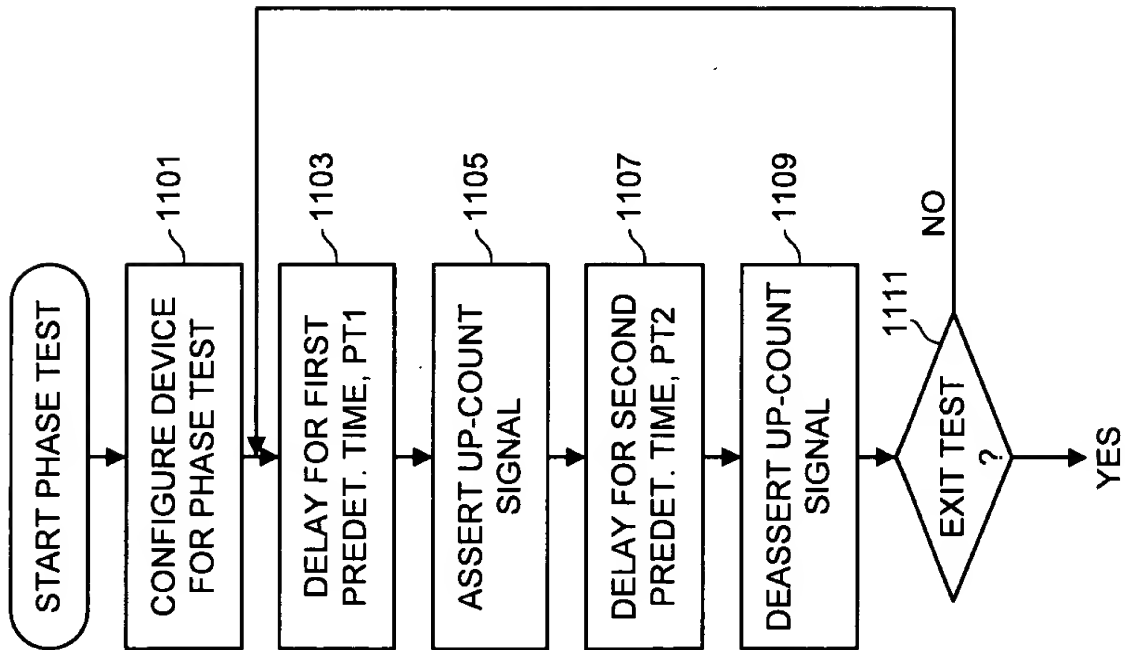
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Patent Application No. 09/088,000
Filed: 03/26/2000
Inventor: [illegible]
Attorney: [illegible]
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FIG. 11



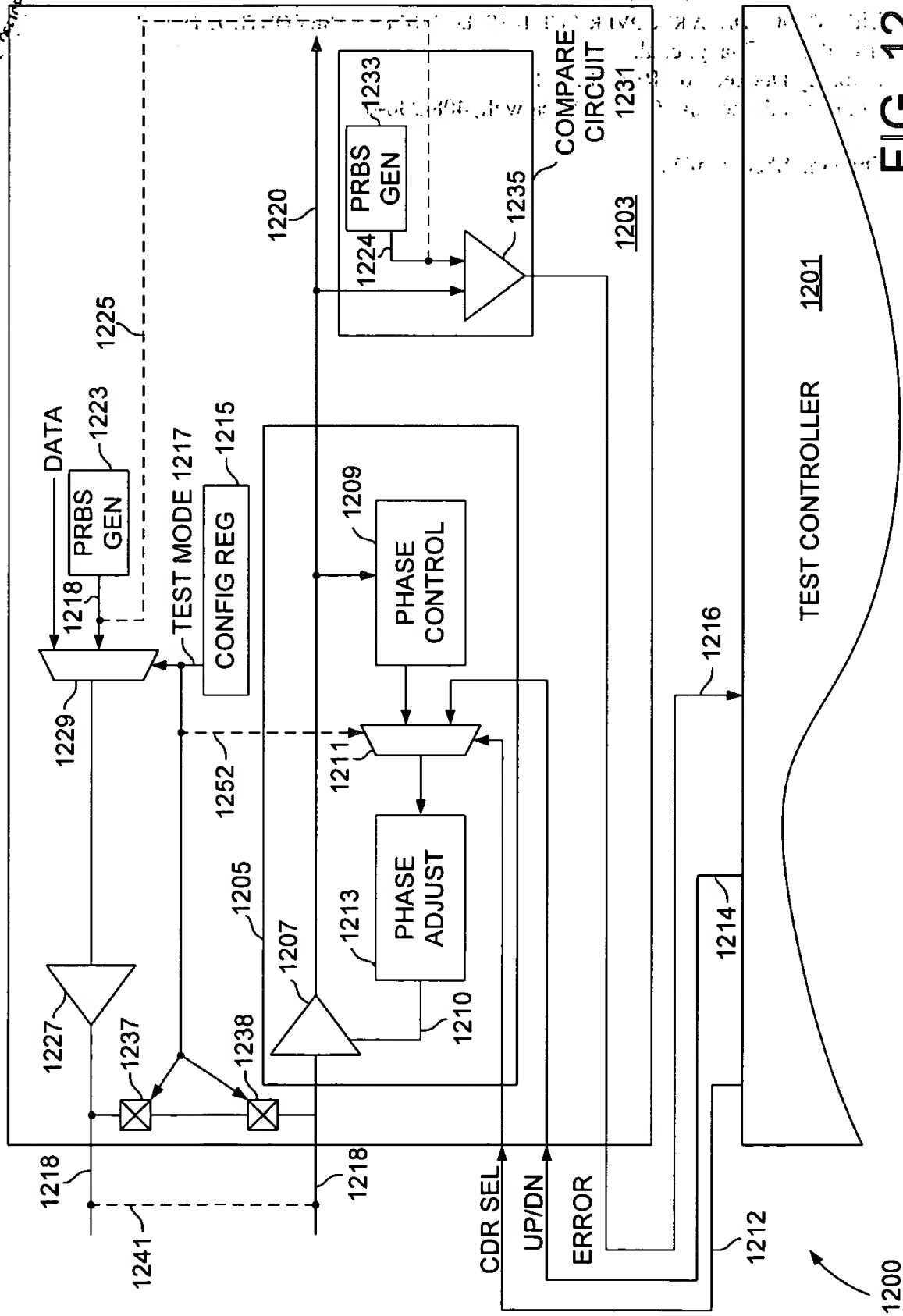
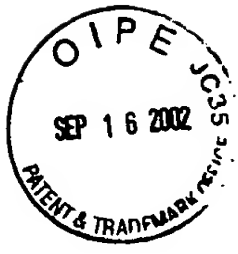


FIG. 12



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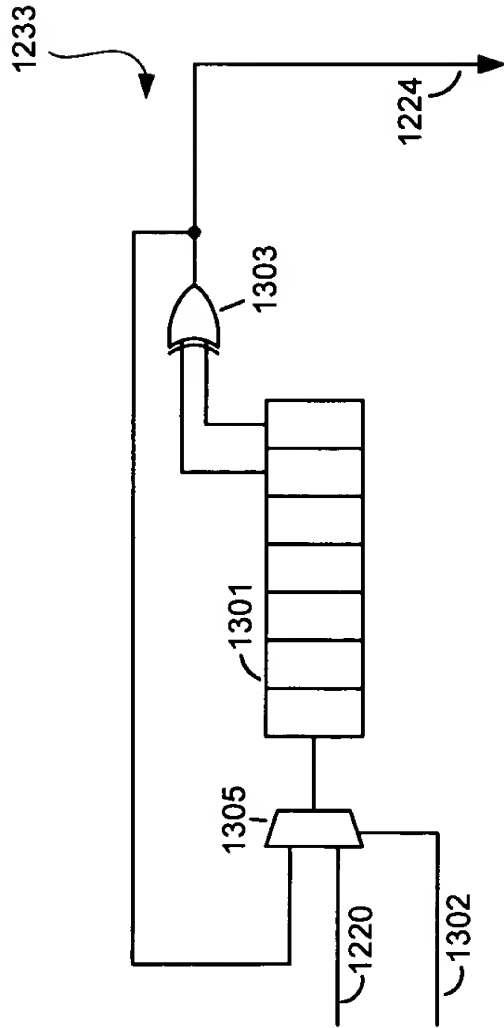
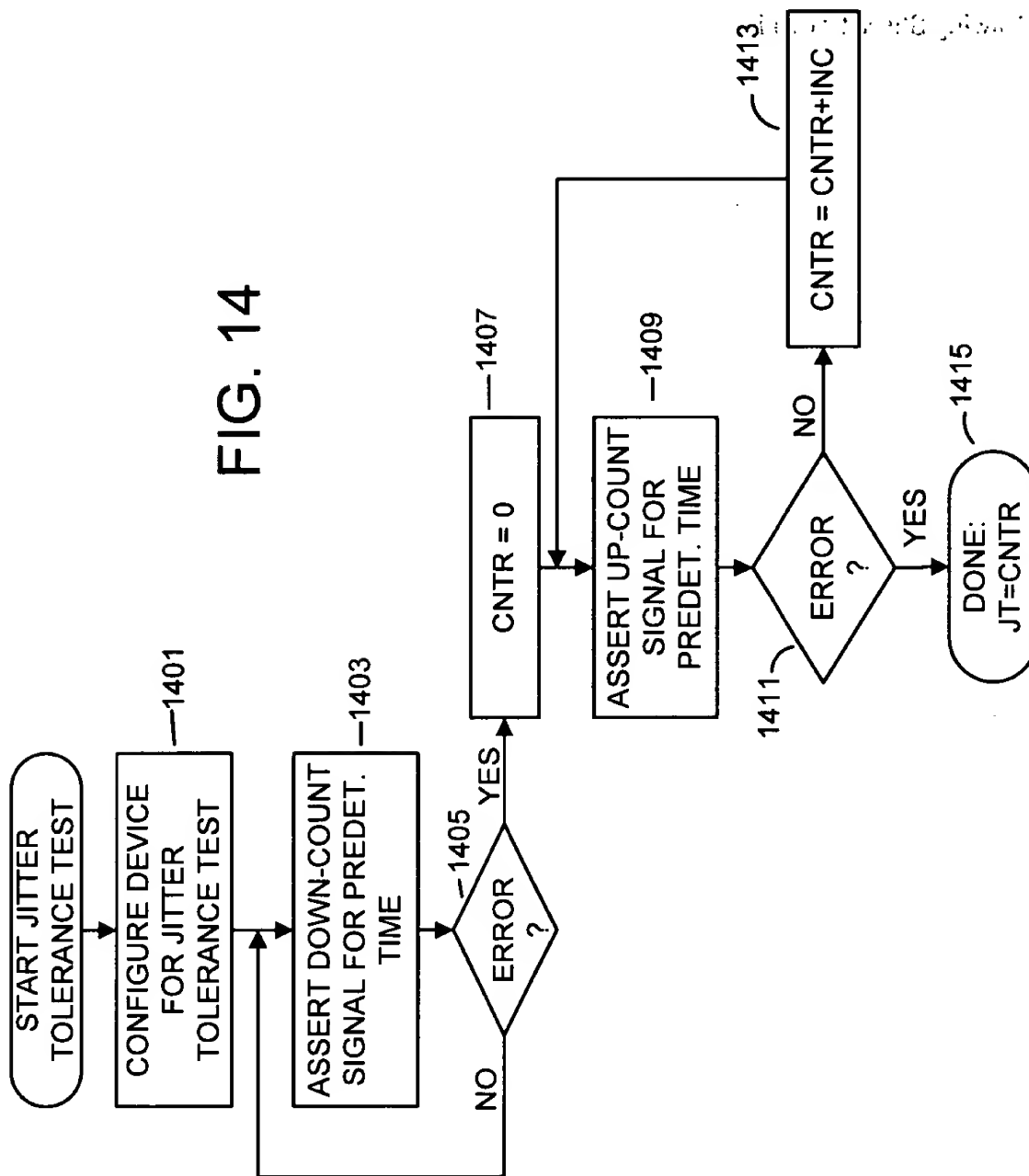


FIG. 13

FIG. 14



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